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PATENT
(5298-04700/PM00028)IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re Application of:
Gilboa et al.§ Group Art Unit: 1763
§ Examiner: Goudreau, G.

Serial No. 09/846,119

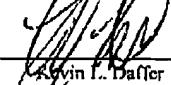
§ Atty. Dkt. No. 5298-04700

Filed: April 30, 2001

For: METHOD OF MAKING A
PLANARIZED SEMICONDUCTOR
STRUCTURE§ I hereby certify that this correspondence is being transmitted
§ via facsimile or deposited with the U.S. Postal Service with
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§ Commissioner for Patents, P.O. Box 1450, Alexandria, VA
§ 22313, on the date indicated below.

01/21/2005

Date

AMENDMENT; RESPONSE TO OFFICE ACTION MAILED NOVEMBER 4, 2004Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Dear Sir/Madam:

This paper is submitted in response to the Office Action mailed November 4, 2004 to further highlight reasons why the application is in condition for allowance.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.